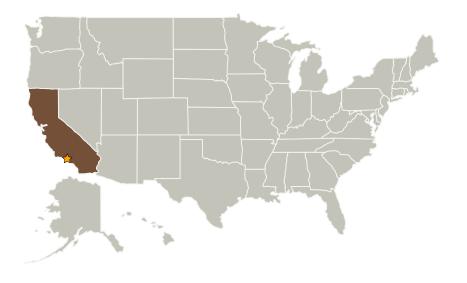
#### **Small Business Innovation Research/Small Business Tech Transfer**

# Radiation tolerant non volatile memory with Hard by Design Techniques, Phase I



Completed Technology Project (2003 - 2003)

#### **Primary U.S. Work Locations and Key Partners**



| Organizations<br>Performing Work | Role         | Туре     | Location   |
|----------------------------------|--------------|----------|------------|
| Jet Propulsion Laboratory(JPL)   | Lead         | NASA     | Pasadena,  |
|                                  | Organization | Center   | California |
| NxGen Electronics,               | Supporting   | Industry | San Diego, |
| Inc.                             | Organization |          | California |

| Primary | U.S. | Work | Locations |
|---------|------|------|-----------|
|         |      |      |           |

California



Radiation tolerant non volatile memory with Hard by Design Techniques, Phase I

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## Organizational Responsibility

# Responsible Mission Directorate:

Space Technology Mission Directorate (STMD)

#### **Lead Center / Facility:**

Jet Propulsion Laboratory (JPL)

#### **Responsible Program:**

Small Business Innovation Research/Small Business Tech Transfer



Small Business Innovation Research/Small Business Tech Transfer

# Radiation tolerant non volatile memory with Hard by Design Techniques, Phase I



Completed Technology Project (2003 - 2003)

### **Project Management**

**Program Director:** 

Jason L Kessler

**Program Manager:** 

Carlos Torrez

**Principal Investigator:** 

Dave J Strobel

### **Technology Areas**

#### **Primary:**

- TX02 Flight Computing and Avionics
  - └─ TX02.1 Avionics
     Component Technologies
     └─ TX02.1.4 High
     Performance Memories

